

Fig. 1 Processor

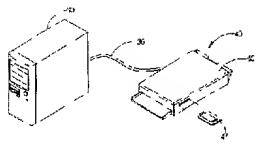
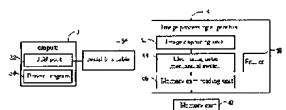


Fig. 2



Exp 3

